The embedded world Exhibition & Conference is the world’s premier event on the embedded systems topic. It brings together researchers and developers, industry and academia from all disciplines of embedded system development and drives the development of complex systems of systems and its manifold innovative aspects.

The embedded world Conference is calling for papers for a special track on "Formal Verification".

Formal verification is now a mature field with significant industrial adoption. Functional safety standards, such as DO-178C, IEC 61508 and ISO 26262, recommend or highly recommend formal verification especially for the most critical safety functions.

Due to the ever increasing complexity of software that is embedded in critical systems to implement safety-relevant functions, the use of formal methods is, by necessity, going to increase across all industries.

Papers are solicited on all aspects of the application of formal verification techniques in industry. Precedence will be given to papers describing:

- formal verification techniques already included in software verification tools;
- formal verification techniques that can be applied without extensive retraining of personnel (e.g., to enable the use of temporal logic specifications);
- experiences of adoption of formal verification into an organization.

www.embedded-world.eu/special-cfps

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Important dates I Submission until 29 September 2023, selecting the topic “6.6 Formal Verification”. Authors will be notified by early December 2023.